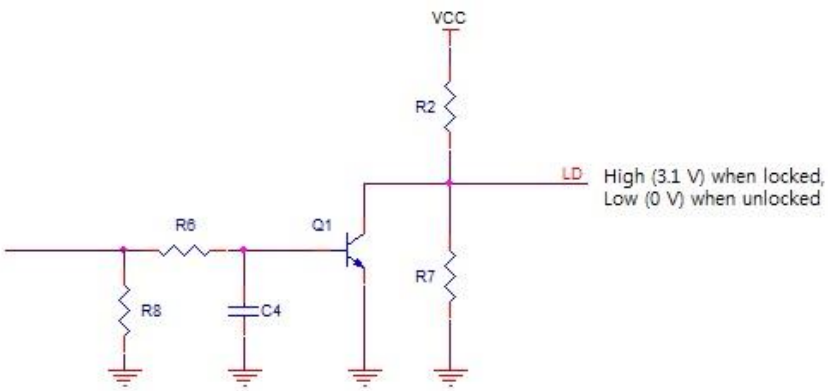
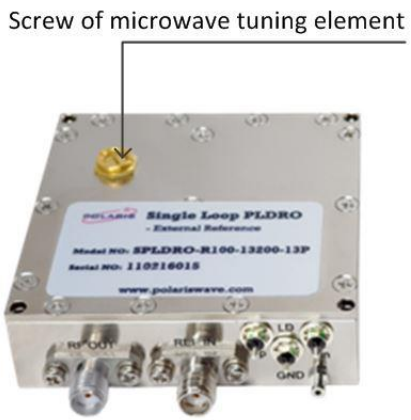
PLDRO Handling Precautions

Be sure to read these Precautions before using the product.
(Figures in this manual are for illustrative purposes only and may differ slightly from the actual appearance.)

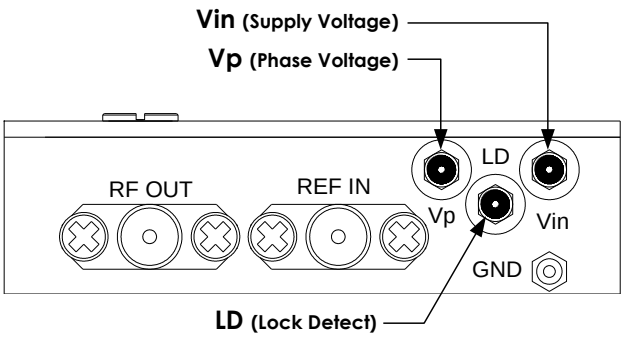
- When an external reference is sourced to this product, the input power should not exceed the rated input range. The product may get damaged.
- When interfacing to the LD (Lock Detect) connector, use a pull-down resistor of 1MΩ or more. See the circuit in [Figure 1].
- Do not adjust the microwave tuning element screw in [Figure 2] without consulting the manufacturer. Phase may be unlocked.
- Connect external cables referring to the connector description in [Figure 3] ~ [Figure 11]. If you use cables thicker than UL1007-24AWG (11/0.16), the connector pins may be broken.
- The cut surfaces of EMI Feed-thru connectors may be sharp, so be careful when touching them.
- Do not apply shock or vibration beyond the specified range.



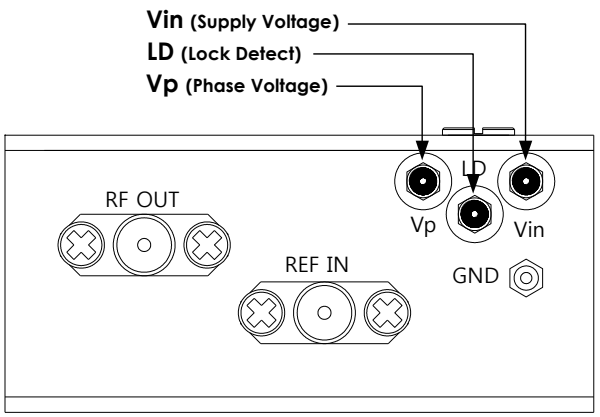
[Figure 1] LD output circuit used inside PLDRO



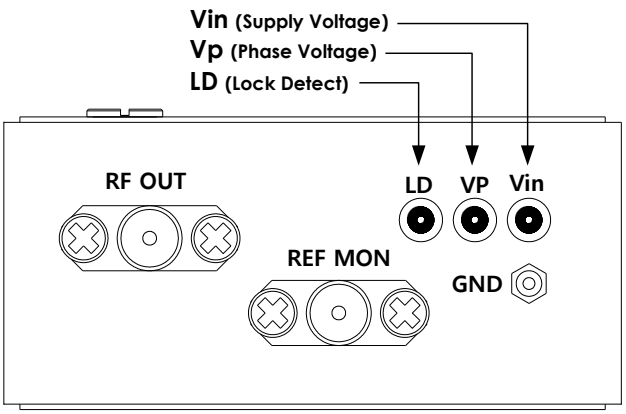
[Figure 2] Screw location of microwave tuning element screw



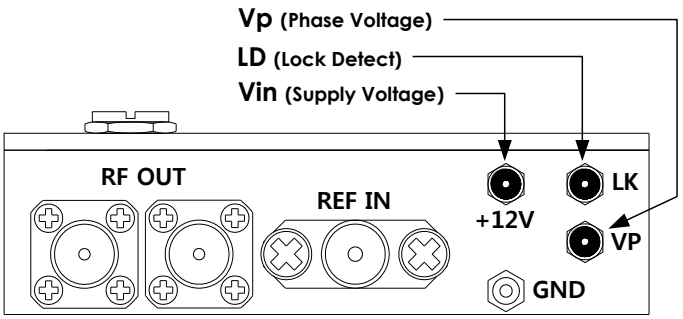
[Figure 3] Single Loop PLDRO using external reference (SB & HS Housing)



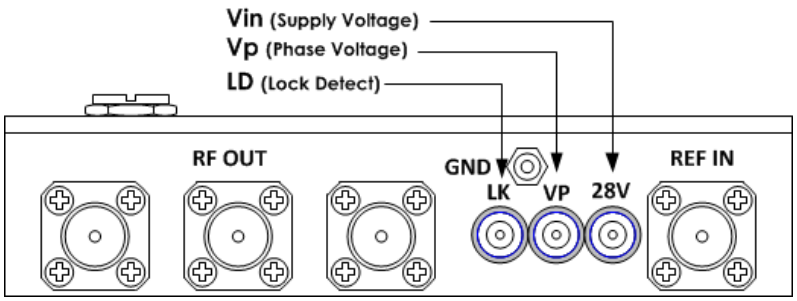
[Figure 4] Single Loop PLDRO using external reference (DB Housing)



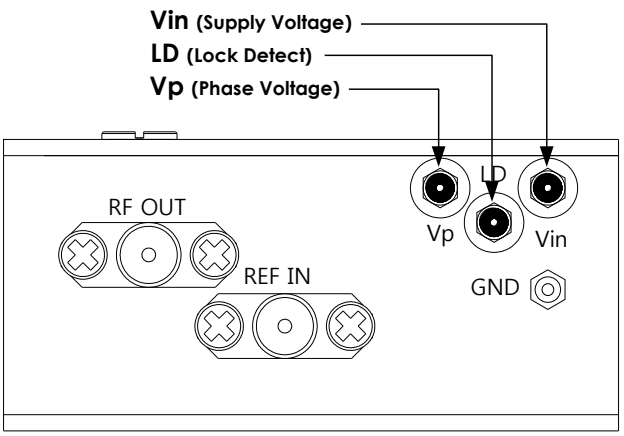
[Figure 5] Single Loop PLDRO using internal reference (Hermetically Sealed DB Housing)



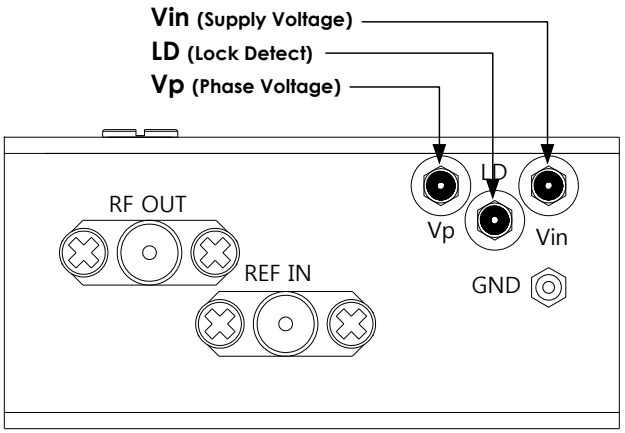
[Figure 6] Dual Channel PLDRO using external reference (SB Housing)



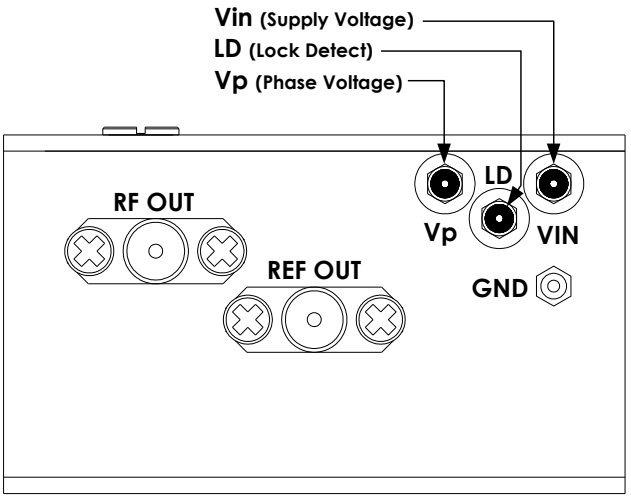
[Figure 7] Triple Channel PLDRO using external reference (SB Housing)



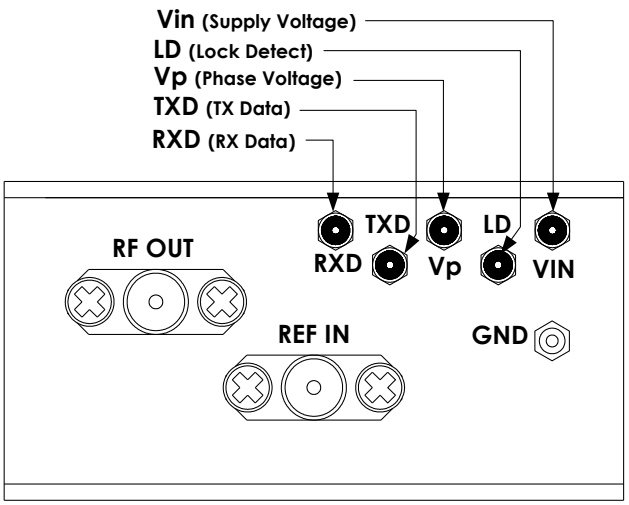
[Figure 8] Dual Loop PLDRO using external reference (DB Housing)



[Figure 9] Fractional-N PLDRO using external reference (DB Housing)



[Figure 10] Fractional-N PLDRO using internal reference (DB Housing)



[Figure 11] SIG-PLDRO using external reference (DB Housing)